



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yang Chin Cheng

/ Group Art Unit:
/ /

Serial No: 10/713,762

/ /
/ Examiner: unknown
/ /

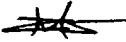
Filed: November 13, 2003

For: METHODS FOR USING A SILYLATION
TECHNIQUE TO REDUCE CELL PITCH
IN SEMICONDUCTOR DEVICES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service, First Class mail, postage prepaid, in an envelope addressed to Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 18, 2004.

STOUT, UXA, BUYAN & MULLINS, LLP


Kenton R. Mullins, Reg. No. 36,331

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. 1.56 and 1.97, Applicants wish to call the attention of the Examiner to the references that are listed on the attached PTO form 1449, copies of which are submitted herewith.

Applicant respectfully requests that the cited references be listed on the face of any patent issuing from this application.

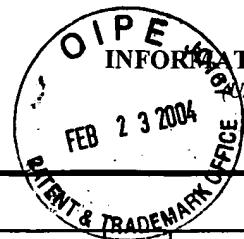
These citations do not constitute an admission that the references are relevant or material to the claims, but rather only constitute the closest art of which Applicants are presently aware.

Respectfully Submitted



Kenton R. Mullins
Attorney for Applicants
Registration No. 36,331

February 18, 2004
4 Venture, Suite 300
Irvine, CA 92618
949-450-1750



EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant